

U.S. Patent Application Serial No. 09/417,705
Amendment dated April 29, 2004
Reply to OA of January 29, 2004

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1 Claim 1 (currently amended): A digital camera, comprising:
2 an imaging device for imaging an object so as to output a first image signal having a first
3 resolution;
4 a thinning-out circuit for thinning out the first image signal so as to create a second image
5 signal having a second resolution lower than the first resolution;
6 a memory having at least two memory areas;
7 a selector for selectively selecting each of said two memory areas;
8 a writer for writing the second image signal outputted from said thinning-out circuit to
9 one of said two memory areas based on a selection result of said selector;
10 a reader for reading the second image signal from the other of said two memory areas
11 based on the selection result of said selector; and
12 a displayer for displaying an image based on the second image signal read out by said
13 reader, wherein said imaging device outputs one screen of the first image signal every first

14 period, and said reader reading out one screen of the second image signal every second period
15 shorter than the first period,
16 the first period is an integral multiple of the second period, and
17 said selector switches a memory area to be selected at an interval of the first period.

Claims 2-5 (canceled)

1 Claim 6 (previously presented): A digital camera according to claim 1, further
2 comprising:
3 an instruction key; and
4 a processor for outputting at predetermined timing a first disable signal, a second disable
5 signal and a third disable signal in response to an operation of said instruction key; wherein
6 said thinning-out circuit being disabled by the first disable signal simultaneously with the
7 operation of said instruction key,
8 said reader being disabled by the second disable signal simultaneously with the operation
9 of said instruction key, and
10 said writer writing the first image signal outputted from said imaging device to said
11 memory and disabled by the third disable signal after one screen of the first image signal has
12 been written.

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1 Claim 7 (previously presented): A digital camera according to claim 6, further
2 comprising a recorder to record the first image signal written on said memory to a recording
3 medium.

1 Claim 8 (previously presented): A digital camera according to claim 7, wherein said
2 processor cancels the second disable signal from outputting after the first image signal has been
3 recorded, and said reader reading the first image signal from said memory.

1 Claim 9 (original): A digital camera according to claim 1, wherein said memory has a
2 single signal input/output port.

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